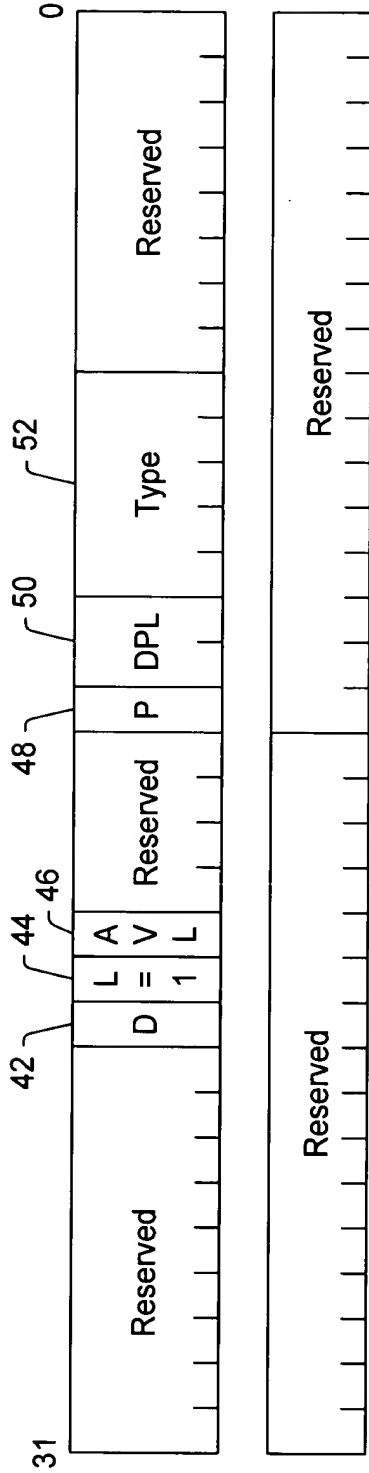
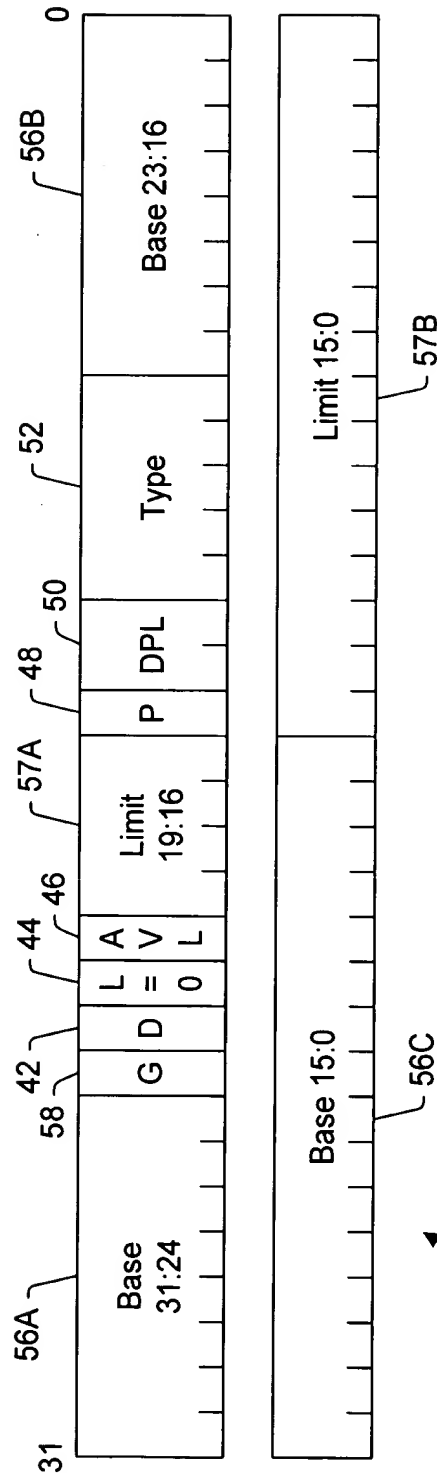


Fig. 1



40


Fig. 2



54

Fig. 3

<u>LMA</u>	<u>CS L Bit</u>	<u>CS D Bit</u>	<u>Operating Mode</u>
0	x	0	16 Bit Mode
0	x	1	32 Bit Mode
1	0	0	16 Bit Compatibility Mode
1	0	1	32 Bit Compatibility Mode
1	1	0	32/64 Mode
1	1	1	Reserved

70 *Fig. 4*

00000000:00000004

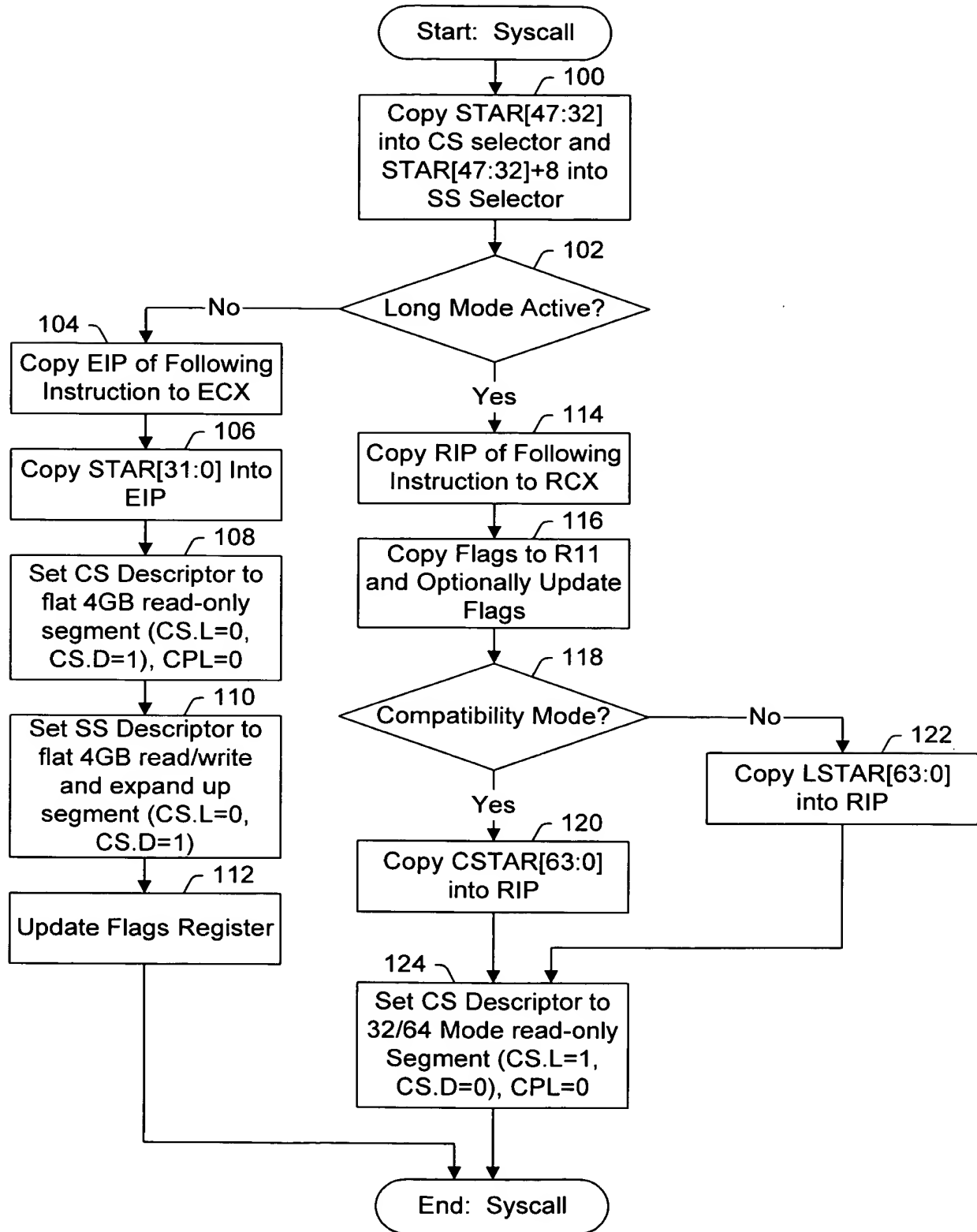
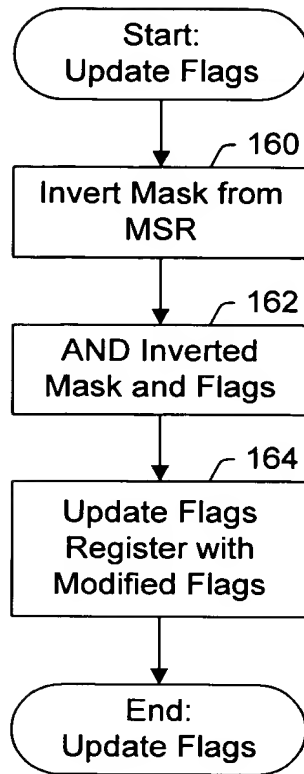


Fig. 5



*Fig. 5A*

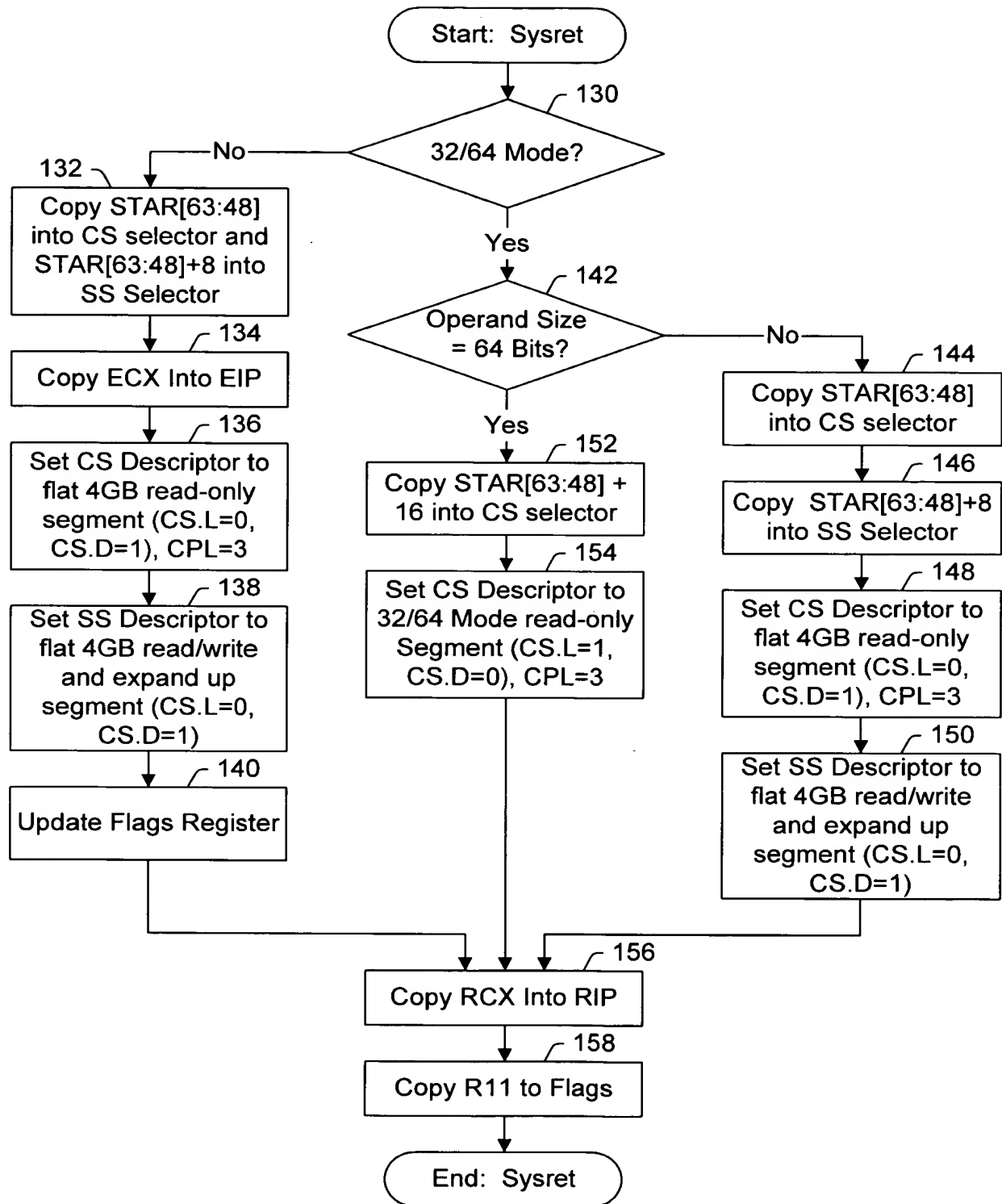
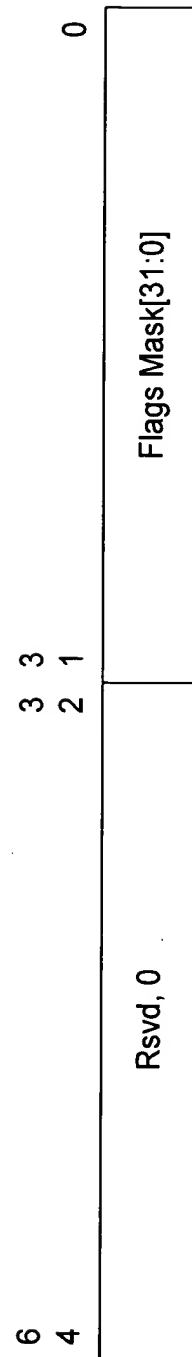
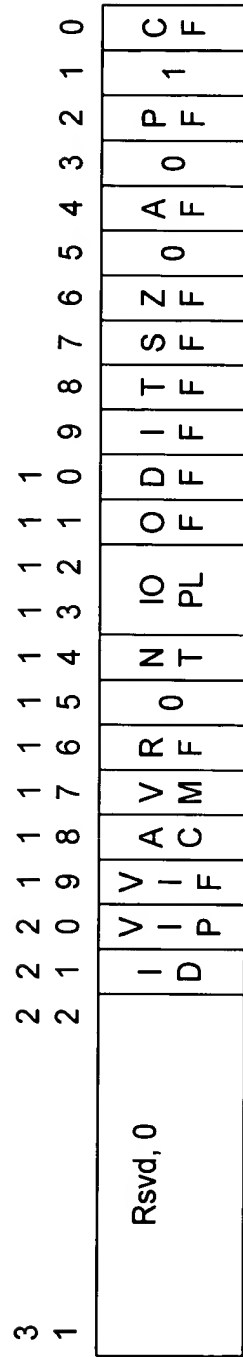
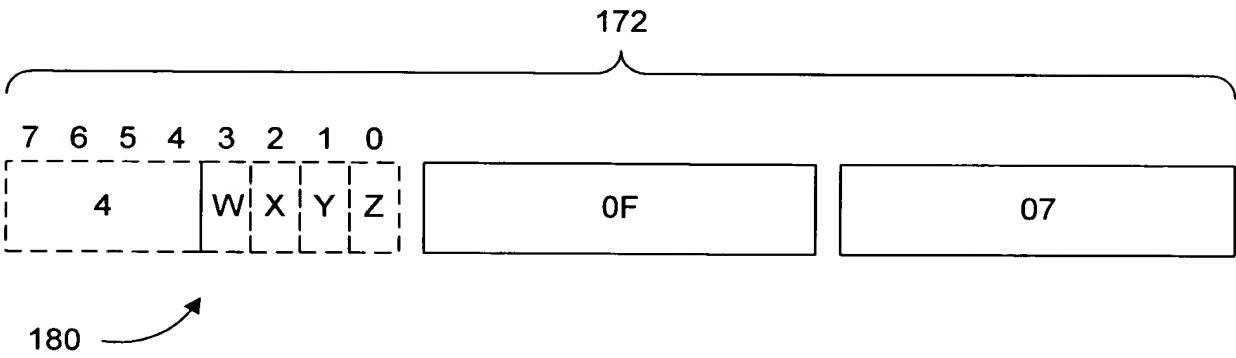
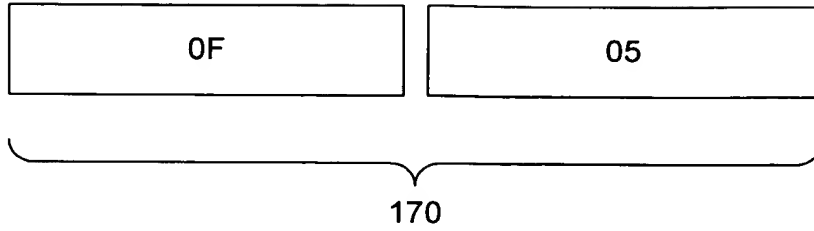


Fig. 6





*Fig. 9*



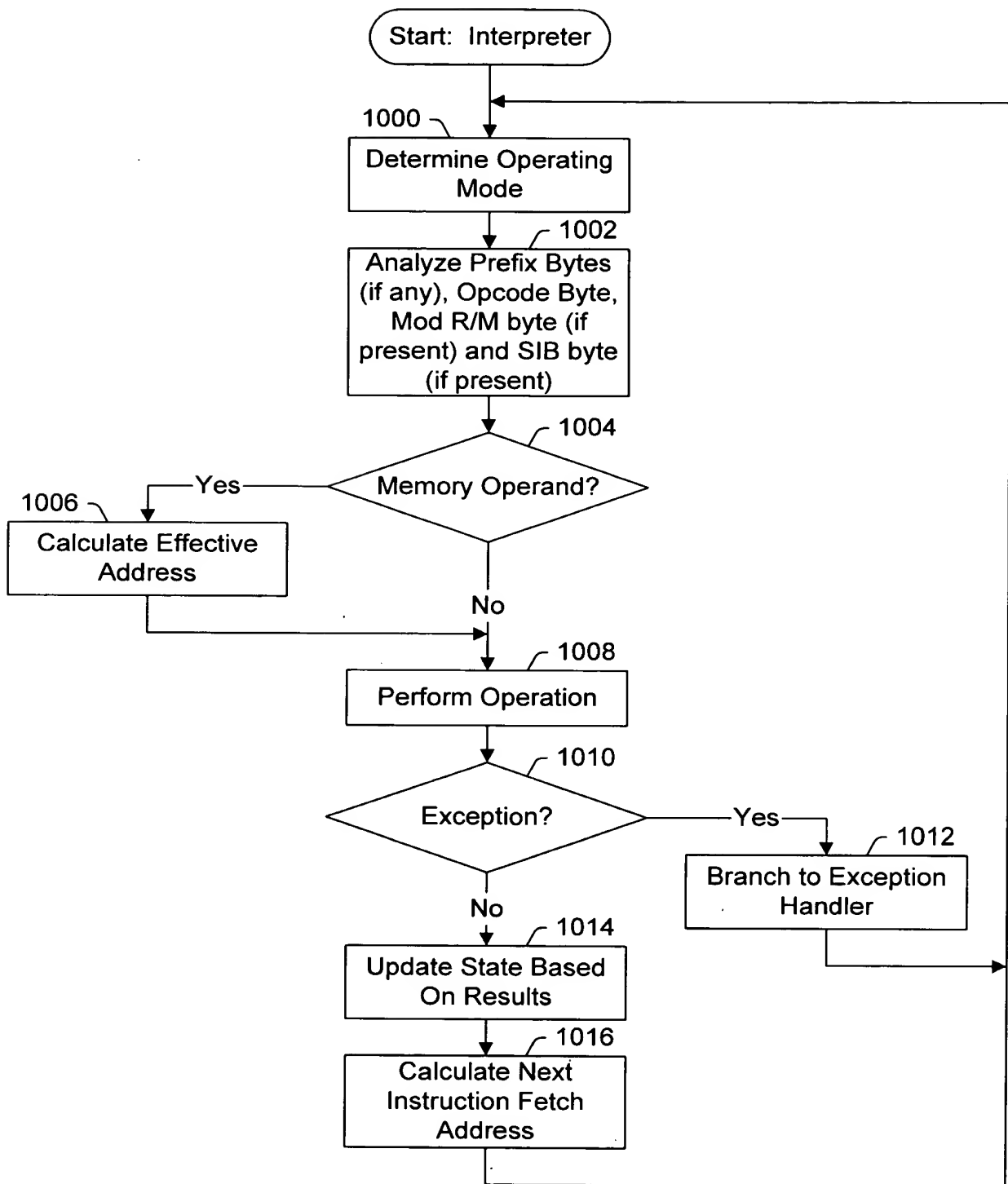
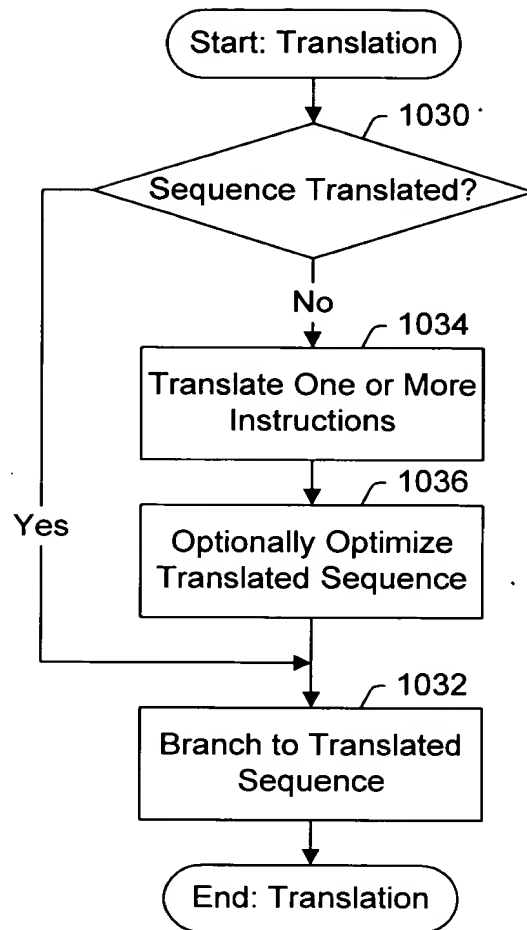


Fig. 10

*Fig. 11*

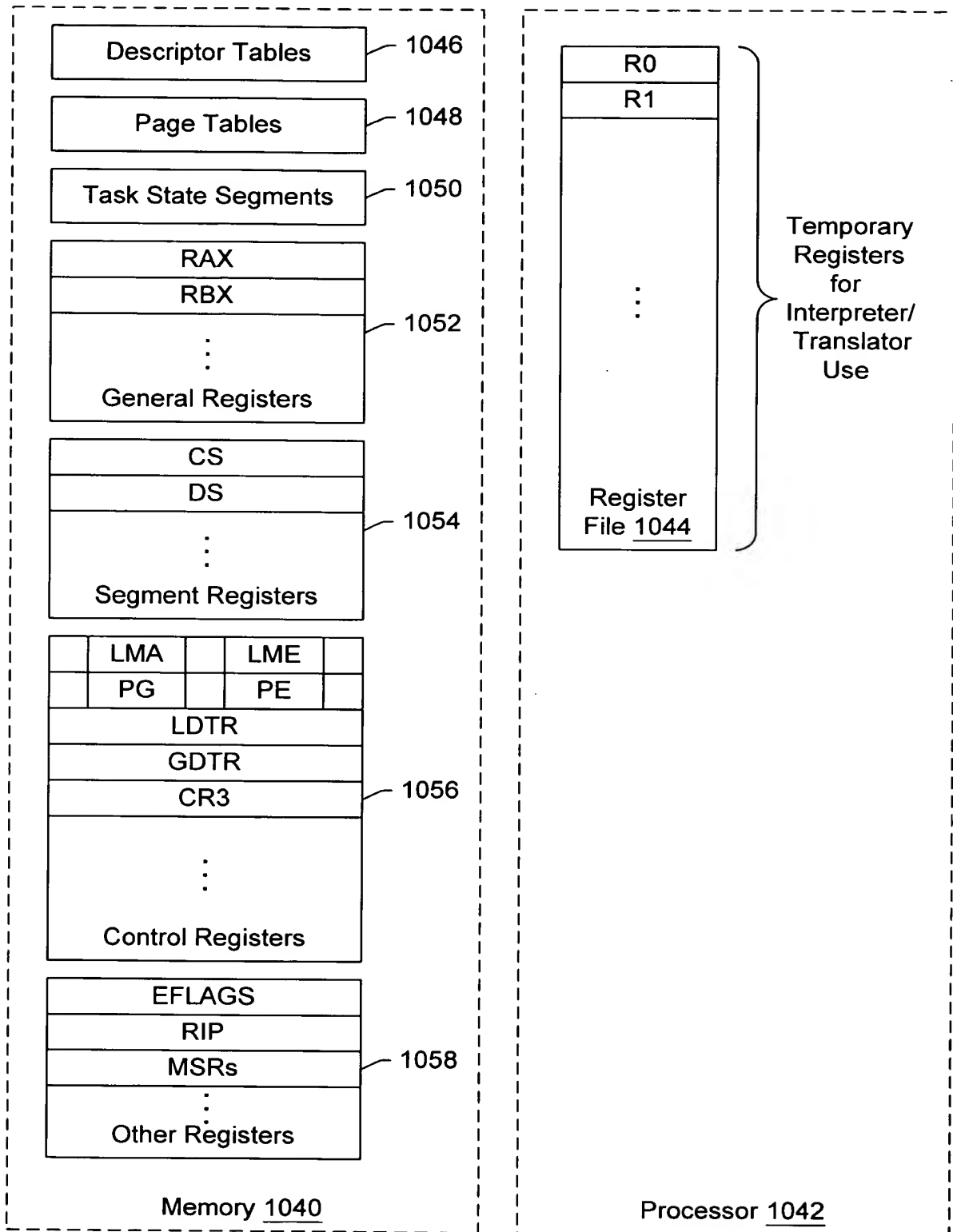


Fig. 12

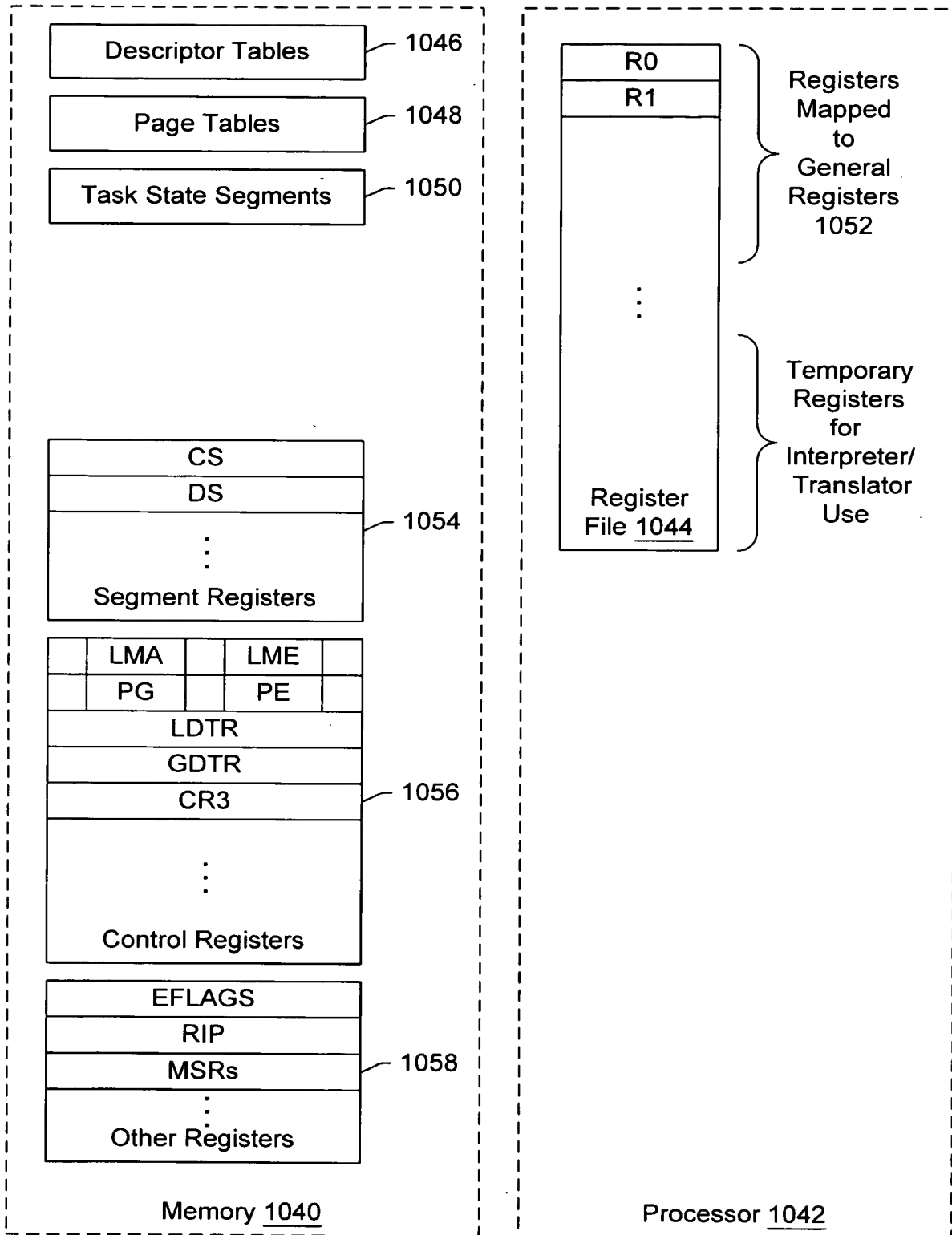


Fig. 13

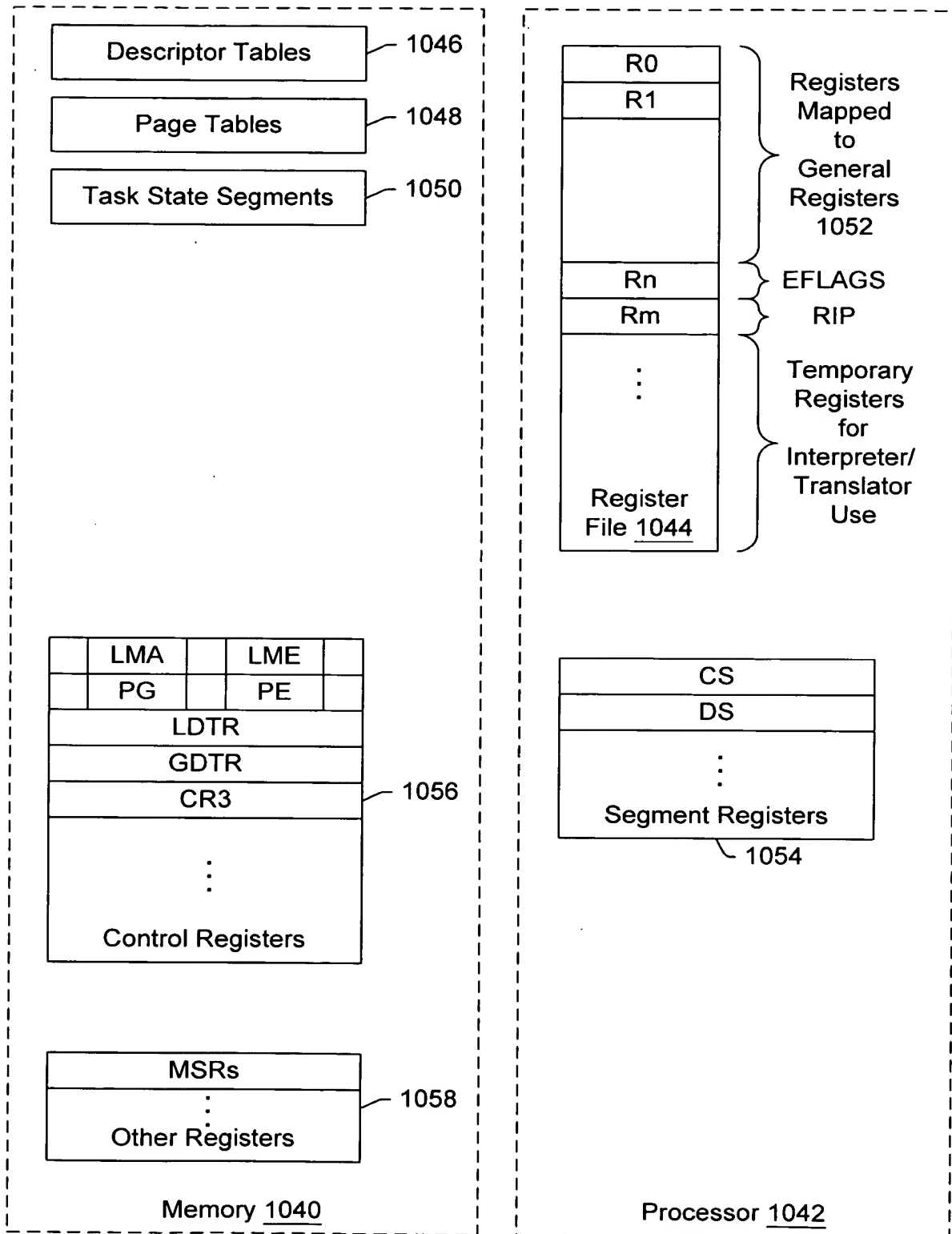
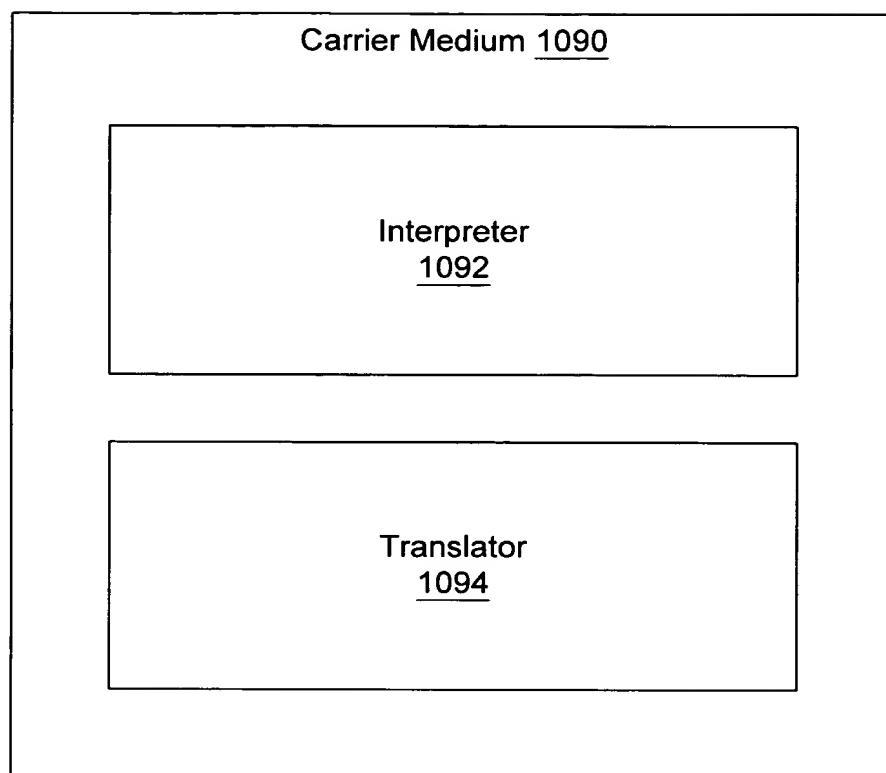


Fig. 14



**Fig. 15**

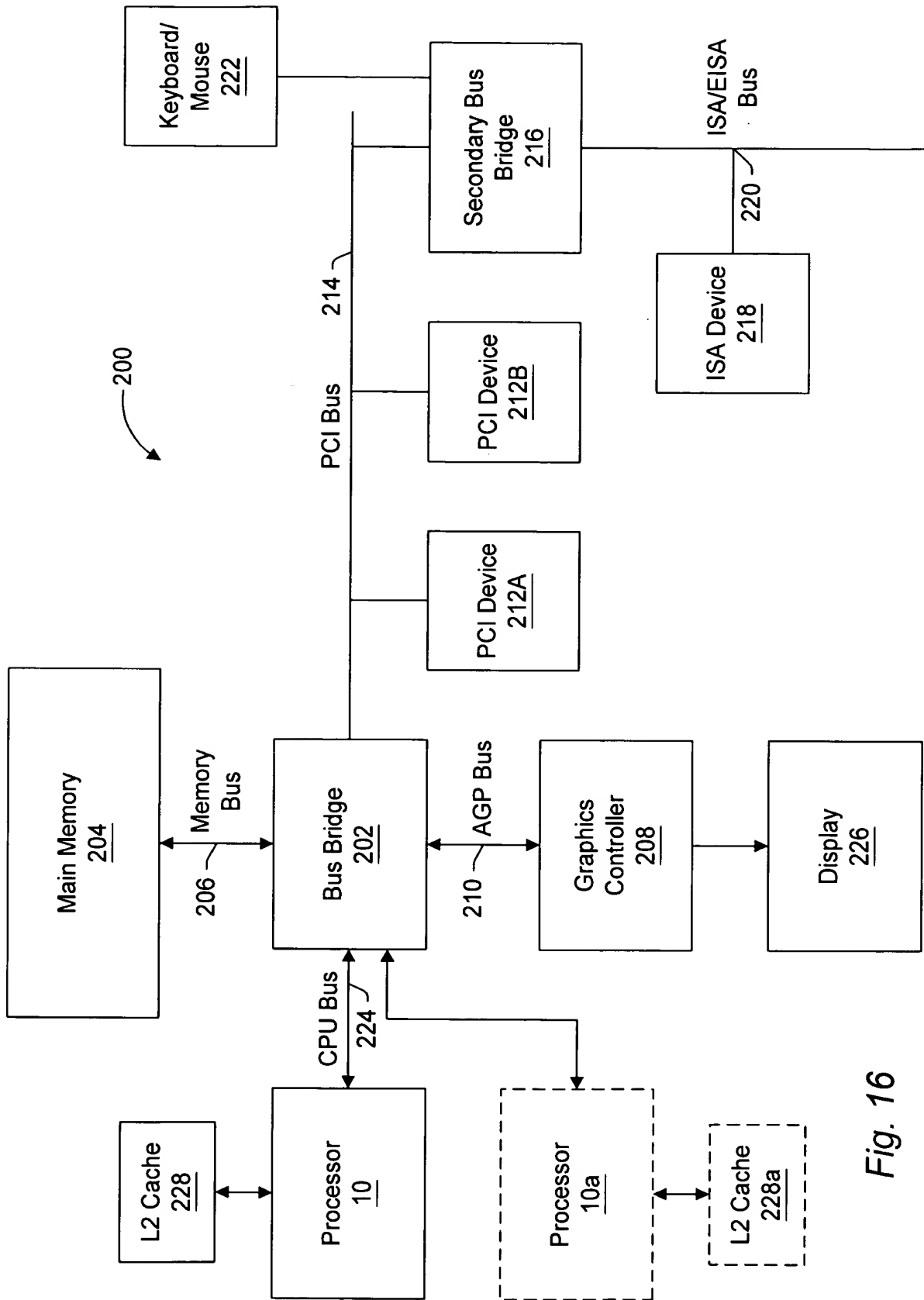


Fig. 16

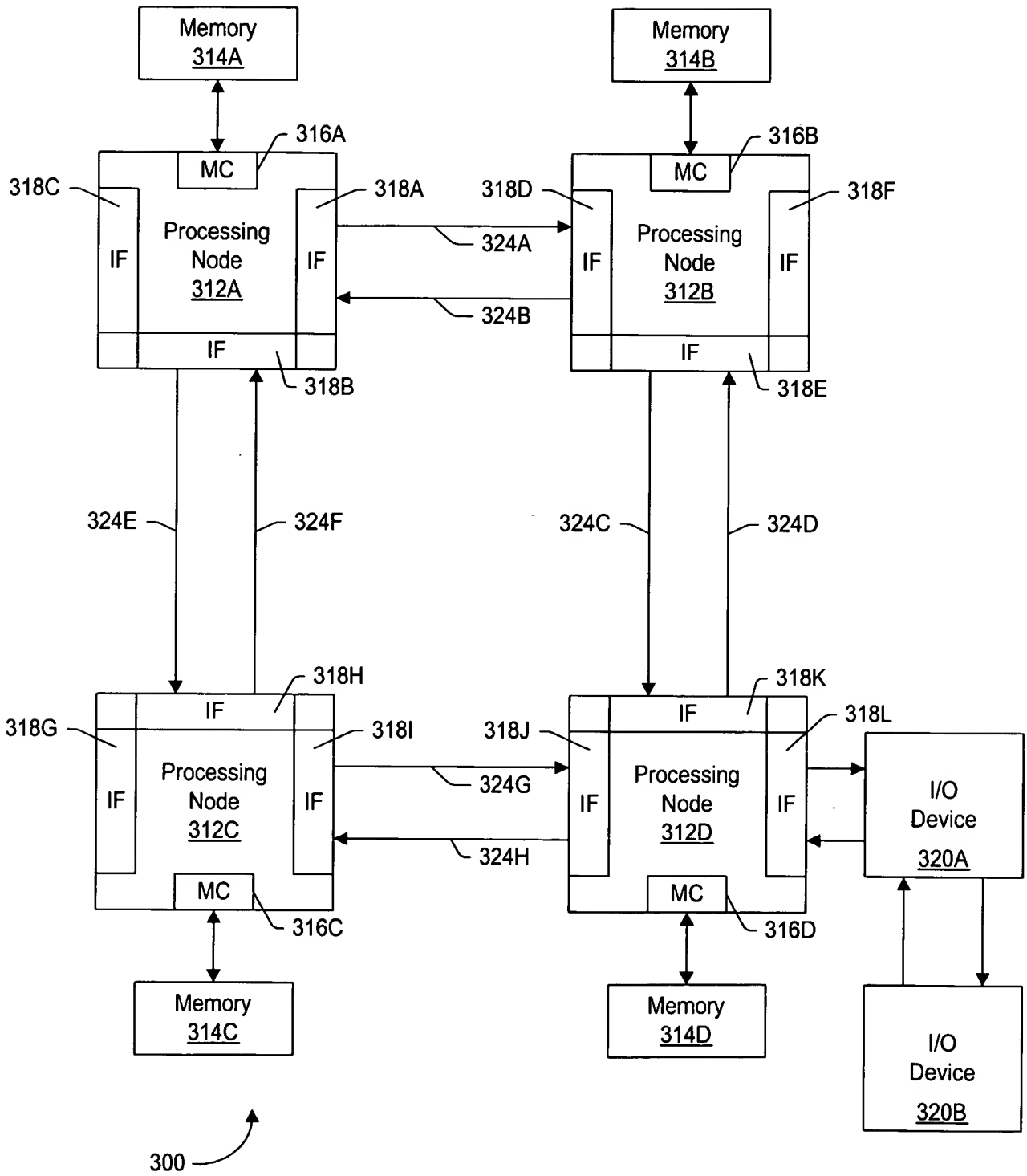


Fig. 17